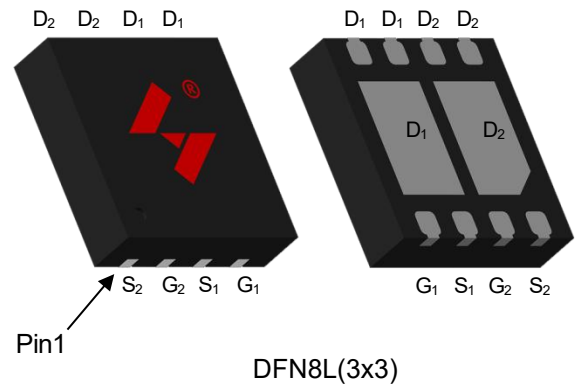


Dual N-Channel Enhancement Mode MOSFET

Feature

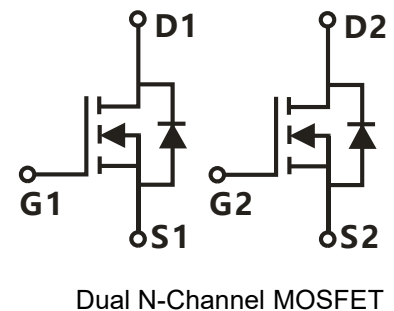
- 40V/26A
 $R_{DS(ON)} = 19\text{ m}\Omega(\text{typ}) @ V_{GS} = 10\text{V}$
 $R_{DS(ON)} = 23\text{ m}\Omega(\text{typ}) @ V_{GS} = 4.5\text{V}$
- 100% Avalanche Tested
- Reliable and Rugged
- Halogen Free and Green Devices Available (RoHS Compliant)

Pin Description



Applications

- Switching Application
- Power Management for DC/DC
- Battery Protection



Ordering and Marking Information

HYG  190ND04LR XYMXXXXX	Package Code C1:DFN8L(3x3) Date Code XYMXXXXX
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Note: HUAYI lead-free products contain molding compounds/die attach materials and 100% matte tin plate termination finish, which are fully compliant with RoHS. HUAYI lead-free products meet or exceed the lead-free requirements of IPC/JEDEC J-STD-020 for MSL classification at lead-free peak reflow temperature. HUAYI defines "Green" to mean lead-free (RoHS compliant) and halogen free (Br or Cl does not exceed 900ppm by weight in homogeneous material and total of Br and Cl does not exceed 1500ppm by weight).

HUAYI reserves the right to make changes, corrections, enhancements, modifications, and improvements to this product and/or to this document at any time without notice.

Absolute Maximum Ratings

Symbol	Parameter		Rating	Unit
Common Ratings (Tc=25°C Unless Otherwise Noted)				
V _{DSS}	Drain-Source Voltage		40	V
V _{GSS}	Gate-Source Voltage		± 20	V
T _J	Junction Temperature Range		-55 to 175	°C
T _{STG}	Storage Temperature Range			°C
I _S	Source Current-Continuous(Body Diode)	Tc=25°C	26	A
Mounted on Large Heat Sink				
I _{DM}	Pulsed Drain Current *	Tc=25°C	78	A
I _D	Continuous Drain Current	Tc=25°C	26	A
		Tc=100°C	18	A
P _D	Maximum Power Dissipation	Tc=25°C	30	W
		Tc=100°C	15	W
R _{θJC}	Thermal Resistance, Junction-to-Case		5	°C/W
R _{θJA}	Thermal Resistance, Junction-to-Ambient **		100	°C/W
E _{AS}	Single Pulsed-Avalanche Energy ***	L=0.1mH	18	mJ

Note: * Repetitive rating; pulse width limited by max.junction temperature.

** Surface mounted on 1in2 FR-4 board.

*** Limited by T_{Jmax}, starting T_J=25°C, L = 0.1mH, R_θ= 25Ω, V_{GS}=10V.

Electrical Characteristics(Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG190ND04LR1			Unit
			Min	Typ.	Max	
Static Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _{DS} =250μA	40	-	-	V
I _{DSS}	Drain-to-Source Leakage Current	V _{DS} =40V, V _{GS} =0V	-	-	1	μA
		T _J =125°C	-	-	50	μA
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _{DS} =250μA	1	1.7	3	V
I _{GSS}	Gate-Source Leakage Current	V _{GS} = ± 20V, V _{DS} =0V	-	-	±100	nA
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =10V, I _{DS} =6A	-	19	25	mΩ
R _{DS(ON)}	Drain-Source On-State Resistance	V _{GS} =4.5V, I _{DS} =6A	-	23	34	mΩ
Diode Characteristics						
V _{SD}	Diode Forward Voltage	I _{SD} =6A, V _{GS} =0V	-	0.84	1.20	V
t _{rr}	Reverse Recovery Time	I _{SD} =6A, dI _{SD} /dt=100A/μs	-	9	-	ns
Q _{rr}	Reverse Recovery Charge		-	4	-	nC

Electrical Characteristics (Cont.) (Tc =25°C Unless Otherwise Noted)

Symbol	Parameter	Test Conditions	HYG190ND04LR1			Unit
			Min	Typ.	Max	
Dynamic Characteristics						
R _G	Gate Resistance	V _{GS} =0V,V _{DS} =0V,F=1MHz	-	2.2	-	Ω
C _{iss}	Input Capacitance	V _{GS} =0V,	-	558	-	pF
C _{oss}	Output Capacitance	V _{DS} =25V,	-	63	-	
C _{rss}	Reverse Transfer Capacitance	Frequency=1MHz	-	51	-	
t _{d(ON)}	Turn-on Delay Time	V _{DD} =20V,R _G =2.5Ω, I _{DS} =6A,V _{GS} =10V	-	7	-	ns
T _r	Turn-on Rise Time		-	13	-	
t _{d(OFF)}	Turn-off Delay Time		-	17	-	
T _f	Turn-off Fall Time		-	4	-	
Gate Charge Characteristics						
Q _g	Total Gate Charge(V _{GS} =10V)	V _{DS} =32V,I _{DS} =6A	-	14	-	nC
Q _g	Total Gate Charge(V _{GS} =4.5V)			7		
Q _{gs}	Gate-Source Charge		-	2	-	
Q _{gd}	Gate-Drain Charge		-	4	-	
V _{plateau}	Gate plateau voltage			-	3.2	-

Note: *Pulse test, pulse width ≤ 300us, duty cycle ≤ 2%

Typical Operating Characteristics

Figure 1: Power Dissipation

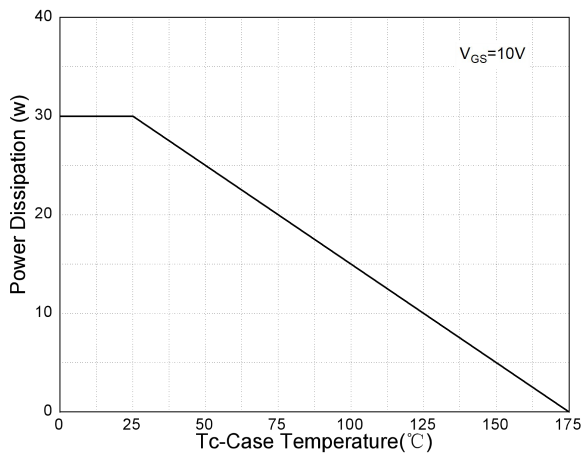


Figure 2: Drain Current

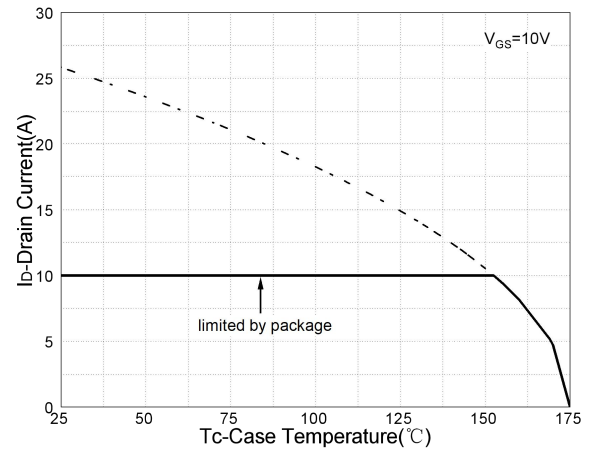


Figure 3: Safe Operation Area

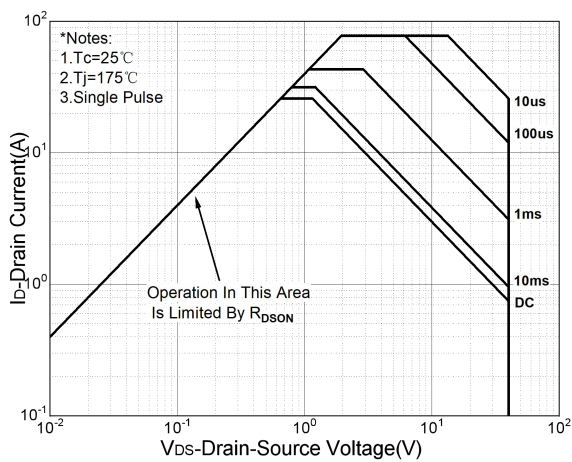


Figure 4: Thermal Transient Impedance

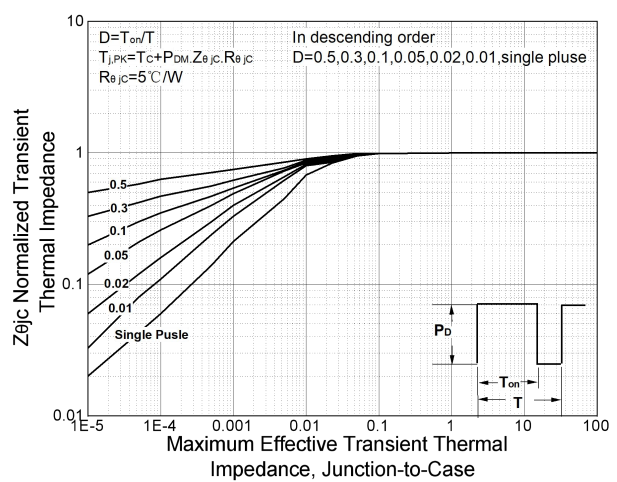


Figure 5: Output Characteristics

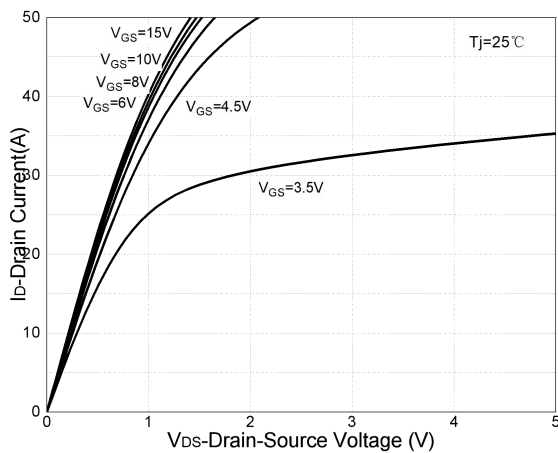
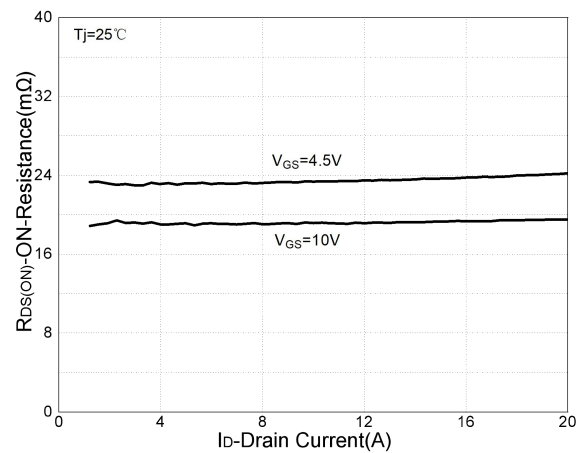


Figure 6: Drain-Source On Resistance



Typical Operating Characteristics(Cont.)

Figure 7: On-Resistance vs. Temperature

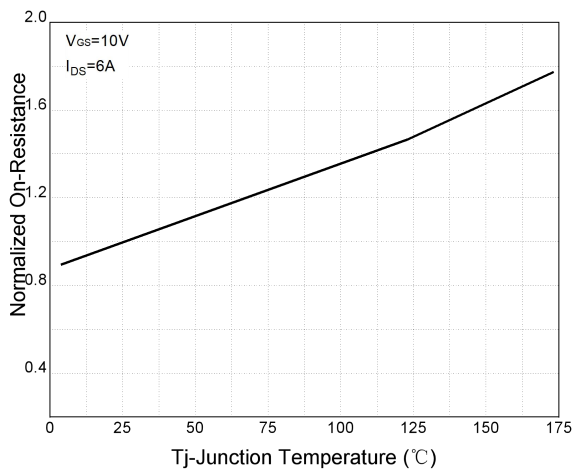


Figure 8: Source-Drain Diode Forward

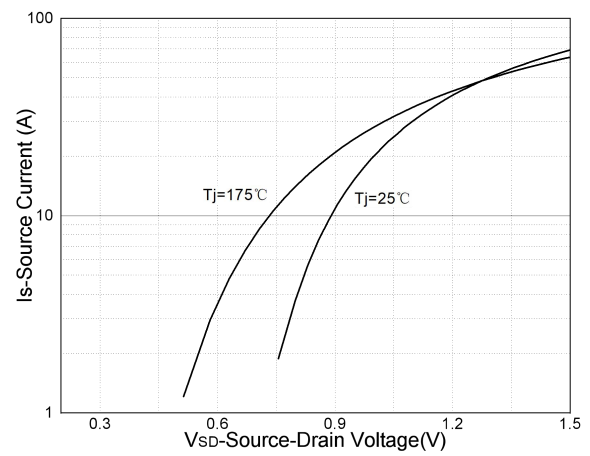


Figure 9: Capacitance Characteristics

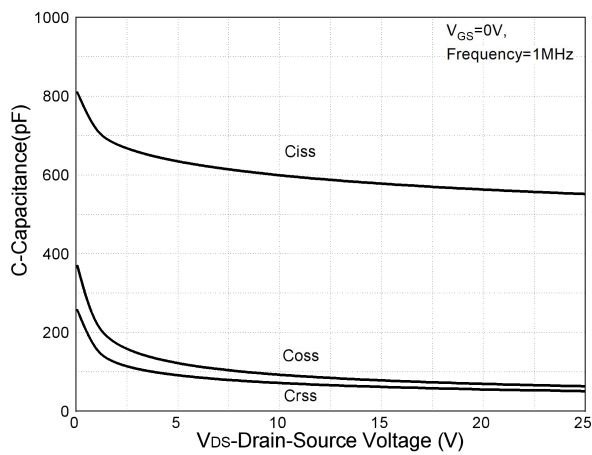
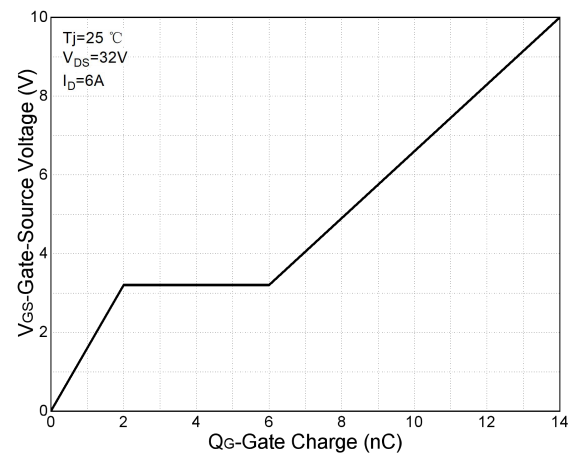
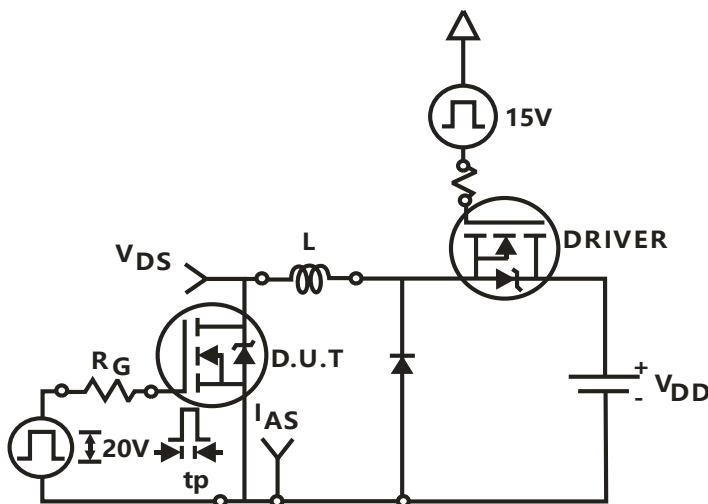


Figure 10: Gate Charge Characteristics

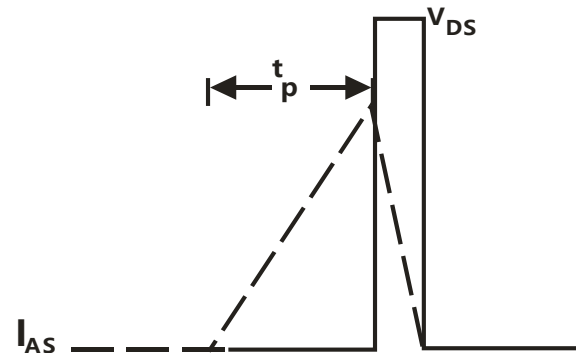


HYG190ND04LR1C1

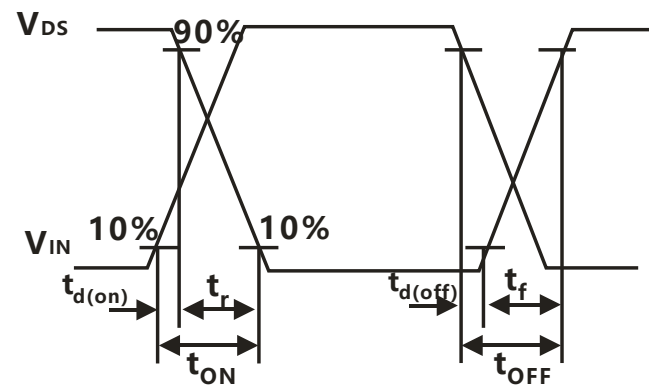
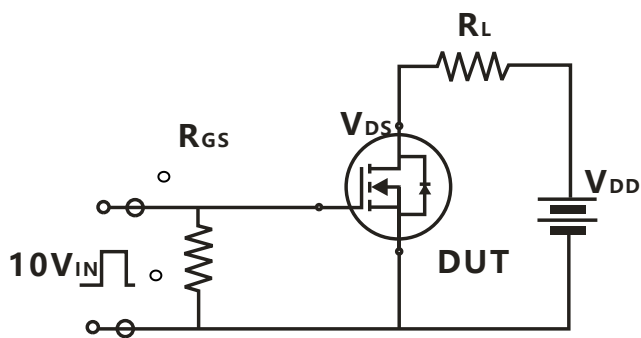
Avalanche Test Circuit



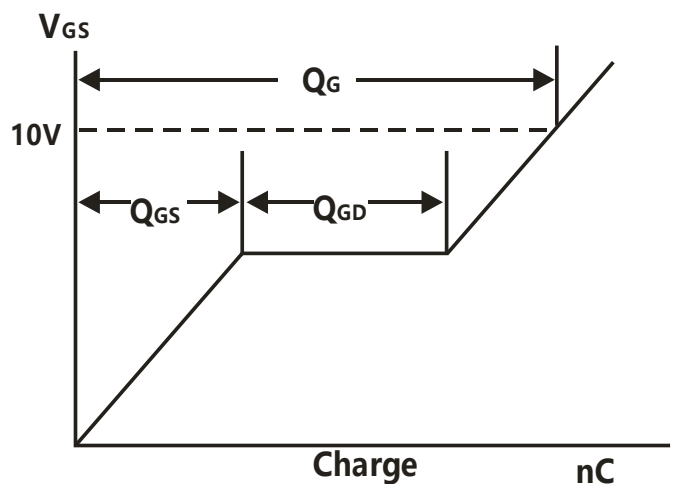
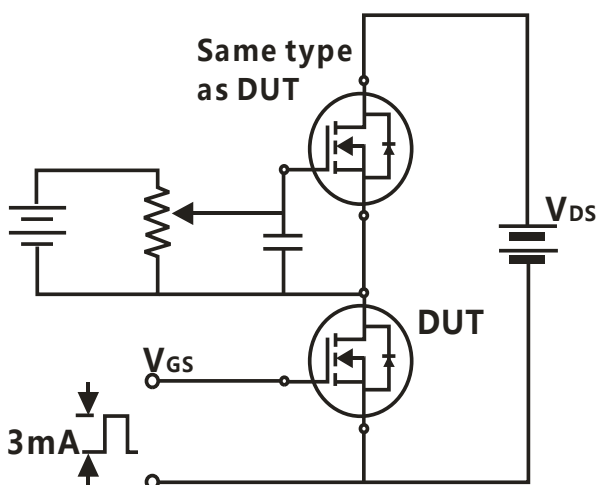
$$E_{AS} = \frac{1}{2} L I_{AS}^2$$



Switching Time Test Circuit



Gate Charge Test Circuit



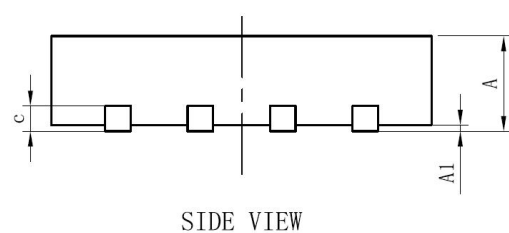
Package Information

Technical drawing of a square component showing three views: TOP VIEW, SIDE VIEW, and BOTTOM VIEW.

TOP VIEW: A square with side length 8. The width is labeled D and the height is labeled E . A laser mark is indicated by a circle and the text "PIN 1# (Lasermark)". The bottom edge is divided into two segments labeled 1 and 2.

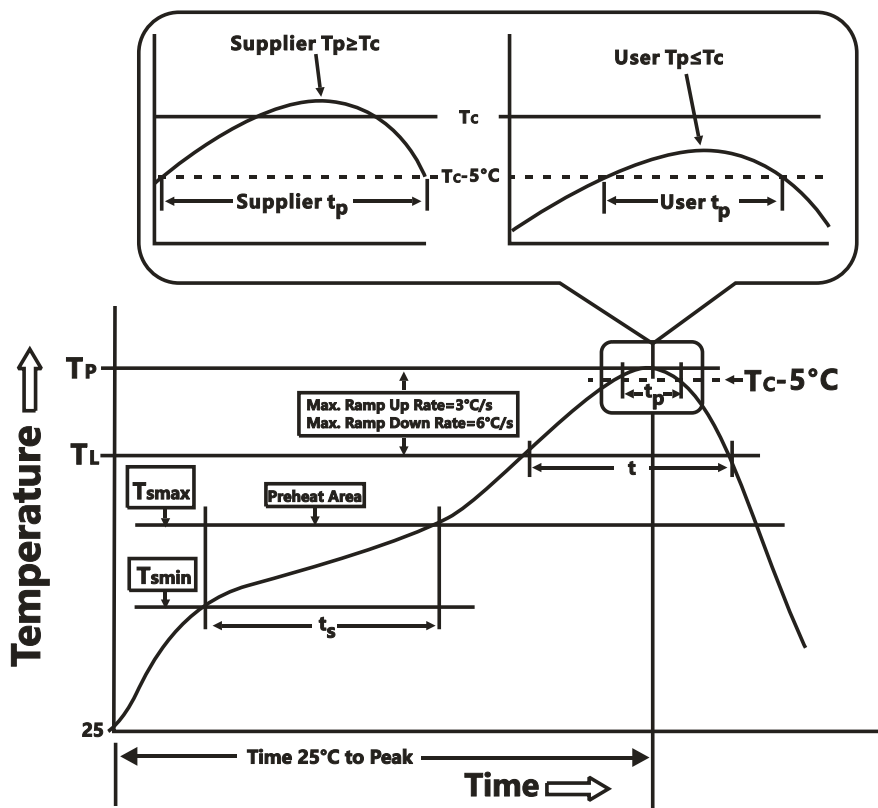
SIDE VIEW: A rectangular profile showing the thickness of the component.

BOTTOM VIEW: A square with side length 8. The width is labeled Nd and the height is labeled $E2$. The bottom edge is divided into two segments labeled 2 and 1. The left edge is labeled L . The right edge is labeled $b1$. The top edge is labeled e and b . The bottom edge is labeled $D2$. A rectangular area is labeled $D3$. A label "PIN 1# hx45°" is shown with a leader line pointing to a corner.



SYMBOL	MILLIMETER		
	MIN	NOM	MAX
A	0.70	0.75	0.80
A1	0.00	0.02	0.05
b	0.25	0.30	0.35
b1	0.20REF		
c	0.18	0.20	0.23
D	2.90	3.00	3.10
D2	0.975	1.075	1.175
D3	0.25REF		
Nd	1.90	1.95	2.00
E	2.90	3.00	3.10
E2	1.60	1.70	1.80
e	0.65BSC		
L	0.35	0.40	0.45
h	0.30	0.35	0.40

Classification Profile



Classification Reflow Profiles

Profile Feature	Sn-Pb Eutectic Assembly	Pb-Free Assembly
Preheat & Soak		
Temperature min (T_{smin})	100 °C	150 °C
Temperature max (T_{smax})	150 °C	200 °C
Time (T_{smin} to T_{smax}) (t_s)	60-120 seconds	60-120 seconds
Average ramp-up rate (T_{smax} to T_P)	3 °C/second max.	3°C/second max.
Liquidous temperature (T_L)	183 °C	217 °C
Time at liquidous (t_L)	60-150 seconds	60-150 seconds
Peak package body Temperature (T_P)*	See Classification Temp in table 1	See Classification Temp in table 2
Time (t_P)** within 5°C of the specified classification temperature (T_c)	20** seconds	30** seconds
Average ramp-down rate (T_P to T_{smax})	6 °C/second max.	6 °C/second max.
Time 25°C to peak temperature	6 minutes max.	8 minutes max.
*Tolerance for peak profile Temperature (T_P) is defined as a supplier minimum and a user maximum.		
** Tolerance for time at peak profile temperature (t_P) is defined as a supplier minimum and a user maximum.		

Table 1.SnPb Eutectic Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ ≥350
<2.5 mm	235 °C	220 °C
≥2.5 mm	220 °C	220 °C

Table 2.Pb-free Process – Classification Temperatures (Tc)

Package Thickness	Volume mm³ <350	Volume mm³ 350-2000	Volume mm³ ≥2000
<1.6 mm	260 °C	260 °C	260 °C
1.6 mm – 2.5 mm	260 °C	250 °C	245 °C
≥2.5 mm	250 °C	245 °C	245 °C

Reliability Test Program

Test item	Method	Description
SOLDERABILITY	JESD-22, B102	5 Sec, 245°C
HTRB	JESD-22, A108	168/500 Hrs, Bias @ 150°C
HTGB	JESD-22, A108	168/500 Hrs, V _{gs} 100% @ 150°C
PCT	JESD-22, A102	96 Hrs, 100%RH, 2atm, 121°C
TCT	JESD-22, A104	250/500 Cycles, -55°C~150°C

Customer Service

Worldwide Sales and Service: sales@hymexa.com

Technical Support: Technology@hymexa.com

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